

Model Predictive Control and Generalized Adaptative PID for Load Sharing in Systems of Multiple Sources of Energy

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Abstract: This paper describes the control of the energy transfers in Systems of Multiple Sources of Energy (SMSE). The considered SMSE is a Linear Time Varying (LTV) system in view of the sources and load fluctuations. The proposed control scheme includes DC bus voltage and current regulations loops as well as the automatic adjustment of the power ratio associated with each source. DC bus voltage and current regulations loops are carried out by generalized adaptive PID controllers. The calculation of the power ratios is computed with a Model Predictive Controller (MPC) designed to optimize the system overall performance

Keywords Generalized adaptative PID, Model control predictive, energy, multi source.

I. INTRODUCTION

Systems of Multiple Sources of Energy (SMSE) [1], [2]. previous works [10] the authors have shown that the duty They are particularly interesting for supplying remote areas Indeed, SMSE may constitute the most economical solution in many applications. In addition, they may result in lower environmental pollution and provide a more reliable supply of electricity through the combination of several energy sources. Thus, the development of SMSE reduces the probability of energy supply shortage. Furthermore, with the incorporation of energy storage, it also reduces the use of diesel generators (which are commonly required in generation systems based on a single renewable energy source). In this context, SMSEs frequently combine solar and wind energy sources (taking advantage of their complementary nature) with a lead-acid battery bank (to overcome periods of scarce power generation). These studies can be gathered into two major categories: design optimization [3], [4], [5] and control optimization, [6], . In the first approach, the main purpose is to reduce the initial investment capital [5], [6]. In the second approach, various strategies are proposed especially for local optimization, coordination of sources and control of the load [1] [7],[8], [9], [10]. The optimization by accepting a load shedding is also studied [11] as well as the availability on the market of equipment [8] In some cases a shedding not larger than 5% may impact strongly on the cost (Bilal and al. 2013).

This work belongs to the second category. Usual controllers include fuzzy logic [12], [10], neural network [13], [14], predictive control [15], [16] and multi objective optimization [1]. Furthermore, several works on hierarchical control have suggested the coupling and decoupling of sources [1], [8], [9]. In this paper, a control scheme is proposed based on a hierarchical approach

In recent years many studies have focused on the control of previously developed by the authors [9]. Indeed, in their cycle value (DC/DC converter input control) is a variable that correlates negatively the available energy and the energy required by the load. They have used this property to automatically adjust the power ratio of each source thanks to a fuzzy logic coordinator [10] and to select the best operating modes with an automaton [9].

> The first contribution is the construction of a SMSE model controlled by the rate of power. In previous works the model was controlled by an analog control voltage input [10]. The second contribution of this paper is to improve the computation of the power ratio with an MPC controller. This coordinator has to find the optimal values of the power ratio of each source with respect to a performance criterion. It is based on an accurate model which describes the coupling of the sources on the DC bus. The problem is solved as a usual quadratic optimization problem.

> The paper is organized as follows: In section II, the topology of the considered SMSE is presented. Section III describes the modeling of sources coupling and presents the local stage of the hierarchical control scheme. The local stage is designed starting from adaptive generalized PID controllers [10] to take into account sources and load fluctuations. The MPC controller which has to compute the optimal values of the power ratios is detailed in section IV. Case studies are simulated and results are presented in section V. Section VI is reserved to the conclusion and perspectives are carried out.

II. TOPOLOGY OF THE SMSE

SMSEs that are studied in this paper are made up of heterogeneous power sources coupled on a DC bus



through DC/DC converters. The supervisory control has been developed [9] for SMSEs with an arbitrary number N of sources and illustrated for N=3 (Figure 1). For simplicity and portability, SMSEs with a conventional source (for example, a diesel generator, named source 1), a renewable source (for example, solar panels, named source 2) and a storage device (for example, a batteries bank, named source 3) will be considered. Such SMSEs cover a large variety of applications and supply variable loads in single-phase or three-phase alternative form (inverter). Sources 1 and 2 are connected on the DC bus via identical Zero Voltage Switching (ZVS) full bridge isolated Buck converters and source 3 is connected on the DC bus with a reversible Buck Boost DC/DC converter. In Figure 1, II j stands for the current of source i, S stands for the DC bus voltage, V_{Ci} stands for the DC/DC converter i analog voltage control input and X_{Di} stands for the logical value of the contactor that couples source i on the DC bus. Gains K_i will be defined in section III.B.

The considered SMSEs may run in several operating modes [9]. Each operating mode corresponds to a configuration that is characterized by the sources that are coupled on the DC bus. As a consequence, 2^N modes will be considered at most for a SMSE with *N* sources [17]. This supervisory control selects the operating mode for a SMSE with 3 sources and has been fully described by the authors in [9].



Figure 1. Topology of the SMSE

An automaton (global stage) selects the best operating mode of the SMSE according to several parameters such as the off periods for the renewable source, the state of charge value for the storage source, the charge sustain, and so on. When an operating mode is selected, the local stage must regulate the DC bus voltage and the current provided by each source according to the power ratio provided by the coordinator. It's important to note that the characteristics of the sources are not required for the proposed control design. In this paper, we propose a new coordination stage. It is based on the use of the Model Predictive Control (MPC) and has to compute the optimal values of the power ratio of each source starting from an accurate model which describes the coupling of the sources on the DC bus. To describe this new coordination stage, mode 6 of [9] (conventional and renewable sources are coupled and battery is feed) is considered.

III. DESIGN OF THE LOCAL STAGE CONTROLLER

A. Modeling of sources coupling

The structural diagram of the ZVS full bridge isolated Buck converter is represented on the Figure 2. These DC/DC converters are isolated (HF transformer TR1) Buck converters (*D5*, *D6*, *D7*, *D8*, *L*, *Ce*, *Re*) with a full bridge (*Q1*, *Q2*, *Q3*, *Q4*) and ZVS. The full bridge control (*Q1*, *Q2*, *Q3*, *Q4*) is realized by a phase shift controller UC3879 through specialized MOSFET drivers IR2113. The duty cycle value \Box is modified by the phase shift between *Va* and *Vb* voltages. The phase shift is controlled by an analog DC voltage (between 0V and 5V) which represents the DC/DC converter analog voltage control input (*VCi*).



Figure 2. Structural diagram of the ZVS full bridge isolated Buck converter

The partial structural diagram when two identical DC/DC converters are coupled on the DC bus (mode 6) of [9] is represented on the Figure 3.



Figure 3. Partial structural diagram of the coupling of two DC/DC converters

State, control and output vectors are respectively defined as $\underline{X}_{\underline{M}} = [I_{Ll}, I_{L2}, S]^T$, $\underline{U}_{\underline{M}} = [V_{Cl}, V_{C2}]^T$, $\underline{Y}_{\underline{M}} = [I_{Llm}, I_{L2m}, S_m]^T$. The average state space model is written with equation (1):

$$\begin{split} & \frac{\mathbf{\dot{X}}_{M}}{\mathbf{X}_{M}} = A_{M} \frac{\mathbf{X}_{M}}{\mathbf{x}_{L}} + B_{M} \frac{U_{M}}{\mathbf{y}_{M}} \\ & \Leftrightarrow \begin{bmatrix} \mathbf{\dot{I}}_{L} \\ \mathbf{\dot{I}}_{L} \\ \mathbf{\dot{S}} \end{bmatrix} = \begin{bmatrix} -\mathbf{r}_{L} & 0 & -\mathbf{I}_{L} \\ -\mathbf{L} & -\mathbf{I}_{L} \\ -\mathbf{L} & \mathbf{L} \\ -\mathbf{I}_{L} & -\mathbf{I}_{L} \\ -\mathbf{I}_{L} & -\mathbf{I}_{L} \\ -\mathbf{I}_{L} & -\mathbf{I}_{L} \\ \mathbf{J}_{L} \\ \mathbf{J}_{L} \\ \mathbf{J}_{L} \\ \mathbf{J}_{M} \end{bmatrix} + \begin{bmatrix} \mathbf{K}_{PS} \mathbf{n} V_{G1} & 0 \\ \mathbf{L} \\ \mathbf{0} & \mathbf{U} \\ \mathbf{J}_{L} \\ \mathbf{J}_{M} \end{bmatrix} \cdot \begin{bmatrix} V_{C1} \\ V_{C2} \end{bmatrix} \\ & (\mathbf{I}) \\ \\ & \sum_{I = 1}^{I} \mathbf{I}_{L} \\ \mathbf{J}_{M} \\ \mathbf{J}_{M} \\ \mathbf{J}_{M} \end{bmatrix} = \begin{bmatrix} \mu_{I} & 0 & 0 \\ 0 & \mu_{I} & 0 \\ 0 & 0 & \mu_{V} \end{bmatrix} \begin{bmatrix} I_{L1} \\ I_{L1} \\ I_{L1} \\ \mathbf{J} \\ \mathbf{J} \end{bmatrix}$$

Note that in comparison with our previous works [9], (Guérin and Lefebvre 2013) this average state space model has been simplified. It doesn't take into account the primary resistance (r_p) , the secondary resistance (r_s) and



the magnetizing inductance (L_M) of the HF transformer The dynamic behavior of each loop (current and voltage) (TR1) as well as the MOSFET transistors (Q_1, Q_2, Q_3, Q_4) can be described [10] with first order closed loop transfer channel resistance (r_{mos}) . These parameters can be functions according to equations (2) to (4): neglected due to their very low values and the estimation of the global losses is not required.

L, r_L , Ce and Re are respectively the coil inductance, the coil resistance, the capacity and the resistance of the Buck converter. *n* is the ratio of the HF transformer. V_{Cl} , V_{C2} are I_{L2} the DC/DC converter analog voltage control inputs (0V/5V). V_{G1} , V_{G2} are the source voltages. K_{PS} is the gain of the phase shift controller UC3879. The duty cycle value S(is proportional to the analog voltage control input so that $\varphi_{l,2}=1$ when $V_{Cl,2}=5V$ ($\varphi_{l,2}=K_{PS}V_{Cl,2}$). R_{CH} is the load (supposed to be resistive). It depends on the current provided to the consumers (Icon) supplied through an inverter and eventually to the current used to charge the batteries bank (Ibb). Note that a current regulator is used to charge the batteries bank so the current *Ibb* is supposed constant. As a consequence, R_{CH} can be modeled as $R_{CH}=S/(Icon+Ibb)$. Note that for simplicity, no batteries bank will be considered in the following. R_{EO} is the equivalent resistive load on the DC bus: $R_{EQ} = (R_{CH} \cdot (Re/2))/(R_{CH} + (Re/2))$. Cdc is the DC bus capacity. C_{EQ} is the equivalent capacitor on the DC bus: $C_{EO}=2Ce+Cdc$. μ_b , μ_V are respectively the gains of the current sensors (LEM-LA55P) and the voltage sensor (LEM-LV25P) associated with their signal conditioners.

B. DC bus voltage and currents regulation loops

The considered SMSE is a Linear Time Varying (LTV) system in view of the sources and load fluctuations. For this reason, adaptive generalized PID controllers have been investigated to control the DC/DC converters under varying operating conditions [10]. The goal is to keep for the voltage and current loops (local stage) the same dynamic behavior (without overshoot or oscillations) whatever the sources and load fluctuations, when the power ratio is modified by the coordination stage.

Under the constraint that the DC bus voltage is constant, it is possible to drive the power delivered to the load by controlling the current provided by each source (load sharing). The cascaded controller (Figure 4) has been designed starting from the average state space model (1) and has been fully described in [10]. It regulates the DC bus voltage (whatever the sources and load fluctuations) and the current provided by each source. It also includes adjustable (between 0% and 100%) gains (K_1, K_2) making it possible to control the power ratio provided by each source.



Figure 4. Local stage controller

$$I_{L1}(s) = \frac{1/\mu_I}{1 + \frac{1}{H_1 \cdot K_{C1} \cdot \mu_I}} J_{REF1}(s) = \frac{1/\mu_I}{1 + \tau_I \cdot s} J_{REF1}(s)$$
(2)

$${}_{2}(s) = \frac{1/\mu_{I}}{1 + \frac{1}{H_{2}.K_{C2}.\mu_{I}}s} J_{REF2}(s) = \frac{1/\mu_{I}}{1 + \tau_{I}.s} J_{REF2}(s)$$
(3)

$$S(s) = \frac{1/\mu_V}{1 + \frac{\mu_I}{K_V.R_{EO}.\mu_V}} S S_{REF}(s) = \frac{1/\mu_V}{1 + \tau_V.s} S_{REF}(s)$$
(4)

$$I_{REF1}(s) = K_1 I_{REF}(s)$$
 $I_{REF2}(s) = K_2 I_{REF}(s)$ (5)

$$I_{REF}(s) = \frac{\mu_{I}.(1 + \tau_{I}.s)}{R_{EQ}}.S(s)$$
(6)

with: $K_1 + K_2 = 1$ and $K_1 > 0$, $K_2 > 0$

 I_{REF1} , I_{REF2} and S_{REF} are respectively the reference values of the current and voltage loops. τ_I is the desired time constant for the current loops. τ_V is the desired time constant for the voltage loop (DC bus). Note that if we neglect the loss of the ZVS full bridge DC/DC converters, the duty cycle values (φ_1, φ_2) can be easily estimated [9] thanks to the following equations:

$$S(s) = n.\varphi_1(s).V_{G1}(s) \quad S(s) = n.\varphi_2(s).V_{G2}(s)$$
(7)

In equation (7), the output voltage (S) is the same for both DC/DC converters since they are coupled on the same DC bus. To conclude, the proposed cascaded adaptive controller doesn't have steady state errors (for both currents and DC bus voltage) and will keep the desired dynamic behavior (τ_I , τ_V) whatever the sources and load fluctuations when the power ratios (K_1, K_2) will be modified by the coordination stage (load sharing).

IV. DESIGN OF THE COORDINATION STAGE CONTROLLER

The coordination stage is based on a Model Predictive Control (MPC) approach. The paradigm of MPC is the use of an internal model to predict the behavior of the system and choose the best decision according to a cost criteria while respecting the functional constraints [18]. MPC is widely used in the field of energy [16]. In the field of the SMSEs it is already used to optimize the production [12] and for supervision control issues [19], [15]. Our approach is based on the use of the duty cycle values of the DC/DC converters $(\varphi_{l}, \varphi_{2})$ as an indirect measurement of the available energy. Indeed, the duty cycle value is a variable that correlates negatively the available energy and the energy required by the load. Indeed, to regulate the DC bus voltage and the current provided by each source, the cascaded adaptive controller previously described, makes increasing the duty cycle values (through VC1, VC2) when the load increases or when the source voltage decreases and inversely [9]. Consequently additional external sensors are not required to estimate the available energy.



Design of the internal model

The MPC controller (coordination stage) has to compute the optimal values of the power ratios (K1, K2). To do that, we need to find an internal model in which the power ratios (K1, K2) belong to the input vector and the duty cycle values (φ 1, φ 2) to the output vector. Over the horizon of prediction, we assume that the DC bus voltage (S) is constant and equal to SREF (the voltage regulation loop doesn't have steady state error). Thus, equation (6) can be rewritten according to the following equation:

$$\mu_I . S_{REF}(s) = R_{EQ} . I_{REF}(s)$$
(8)

Starting from equations (2) to (8), the internal model can be written according to equation (12). State, control and output vectors are respectively defined as:

$$X=[IL1, IL2, S]T$$
(9)

$$U = [K1, K2]T$$
 (10)

$$Y = [IL1, IL2, S, \phi 1, \phi 2]T$$
(11)

$$\frac{\dot{X}}{S} = A \underline{X} + B \underline{U}$$

$$\Leftrightarrow \begin{bmatrix} i_{L_{1}} \\ i_{L_{1}} \\ s \end{bmatrix} = \begin{bmatrix} -\frac{1}{\tau_{i}} & 0 & 0 \\ 0 & -\frac{1}{\tau_{i}} & 0 \\ 0 & 0 & -\frac{1}{\tau_{v}} \end{bmatrix} \begin{bmatrix} I_{L_{1}} \\ I_{L_{2}} \\ s \end{bmatrix} + \begin{bmatrix} \frac{S_{\text{REF}}}{R_{\text{EQ}}\tau_{i}} & 0 \\ 0 & \frac{S_{\text{REF}}}{R_{\text{EQ}}\tau_{i}} \end{bmatrix} \begin{bmatrix} K_{i} \\ K_{2} \end{bmatrix}$$

$$\underline{Y} = C \underline{X} + D \underline{U}$$

$$\Leftrightarrow \begin{bmatrix} I_{L_{1}} \\ S \\ \varphi_{1} \\ \varphi_{2} \end{bmatrix} = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 0 & \frac{1}{nV_{C_{1}}} \\ 0 & 0 & \frac{1}{nV_{C_{2}}} \end{bmatrix} \begin{bmatrix} I_{L_{1}} \\ I_{L_{2}} \\ S \end{bmatrix} \quad D = 0$$
(12)

In discrete time (sampling period Te), equation (12) is rewritten according to a first order approximation:

$$X_{k+1} = F X_k + G U_k$$

$$Y_k = \mathbf{C} \cdot X_k + D U_k$$
(13)

with: F = I + A. Te and G = B. Te

I is the identity matrix of appropriate dimensions.

B. Formulation of the cost criteria

The previous internal model is used to predict the behavior of the system and choose the best decision according to a cost criteria while respecting the functional constraints. The cost criteria (J) to minimize is the distance between the reference output YREF and output Y:

$$J_{k} = \sum_{j=1}^{N} \left(Y_{k+j} - Y_{REF} \right)^{T} \cdot \left(Y_{k+j} - Y_{REF} \right)$$
(14)

Where N is the horizon of prediction. The desired output vector $YREF = (IL1REF IL2REF SREF \phi 1REF \phi 2REF)T$ is selected according to technical considerations.

Let us assume that the sources, the load, and the control vector remain constant over the horizon of prediction, the system output Y (12) is predicted over N steps with the following equations:

$$X_{k+n} = F^{n} \cdot X_{k} + \left(\sum_{j=0}^{j=n-1} F^{j}\right) \cdot G \cdot U_{k}$$

$$Y_{k+n} = C \cdot F^{n} \cdot X_{k} + \left[C \cdot \left(\sum_{j=0}^{j=n-1} F^{j}\right) \cdot G + D\right] \cdot U_{k}$$
(15)

After having remove all constant terms (with respect to Uk), the expression of the cost criteria is rewritten as:

$$J_{k+n} = \frac{1}{2} U_k^T H_{k+n} U_k + f_{k+n} U_k$$
(16)

with:

$$H_{k+n} = 2 \cdot \left[C \cdot \left(\sum_{j=0}^{j=n-1} F^{j} \right) \cdot G + D \right] \cdot \left[C \cdot \left(\sum_{j=0}^{j=n-1} F^{j} \right) \cdot G + D \right]^{T}$$

$$f_{k+n} = \left[Y_{k}^{T} \left(C \cdot F^{n} \right)^{T} - Y_{ref}^{T} \right] \cdot \left[C \cdot \left(\sum_{j=0}^{j=n-1} F^{j} \right) \cdot G + D \right]$$
(17)

that leads to a quadratic cost criteria (with respect to Uk):

$$J_{k} = \frac{1}{2} \sum_{j=1}^{N} \left(U_{k}^{T} H_{k+j} U_{k} + f_{k+j} \cdot U_{k} \right)$$
(18)

The cost criteria (J) is minimized (with respect to Uk) by using a standard quadratic optimization method. The following constraints are required by the load sharing:

•
$$K1 + K2 = 1$$

• Ki <1

V. SIMULATIONS AND RESULTS

To highlight the performances of the controllers (local and coordination stages), several Matlab/Simulink simulations have been done with the following parameters: L=120 μ H, rL=0.5 Ω , Ce=330 μ F, Re=56K Ω , Cdc=1 μ F, n=10, μ I=1V/A, μ v=6.25mV/V, KPS=0.2V-1. In addition, Te=0.15ms is the sampling period. For the current and voltage regulation loops the desired time constants are respectively τ i=20ms, τ v=200ms. The load is supposed constant: RCH=30 Ω (Ibb=0A, Icon=10A for example).

The desired output vector is: $YREF = (8\ 2\ 300\ 0.5\ 0.5)T$

Notes:

- SREF=300V is the desired DC bus voltage.
- φ1,2REF=0.5 corresponds to duty cycle values chosen (running point) in the middle of the range (phase shift controller UC3879).

The source voltages (VG1,VG2) are represented in Figure 5. They varies between the maximum (120V) and minimal (30V) values tolerated by the DC/DC converter. The adaptive generalized PID adjust the duty cycles values (Figure 6) in order to regulate the DC bus voltage (Figure 7) and the current provided to the load by each source (Figure 10). One can notice that the variations of the power ratio and the source voltages do not disturb the regulation loops.

The MPC calculates the optimal power ratios K1 and K2 (Figure 9). The power ratios are automatically calculated



and adjusted at each time step. The profile of each source current (Figure 10) is similar to the profile of the power ratios (Figure 9). The sum of the source currents (Figure 10) is equal to the current required by the load (Figure 8).

In this work we simulated the situation where the source voltages vary in normal operating conditions. Sometimes the source voltage 2 is the double of the source voltage 1 (for example, at time instant 5s).

We can see that the MPC controller calculate the best power ratios in order to keep the duty cycle values near to 0.5. It always maintain them between 0.2 and 0.8. This property is important. Indeed the authors [10] were shown that the available energy from the source is function of duty cycle. For any variation, the system can be adjusted



Figure 8. Load current



VI. CONCLUSION AND PERSPECTIVES

In this paper, a control scheme that includes adaptive generalized PID and model predictive control has been proposed to improve the load sharing in SMSEs. The local controllers cooperate with a MPC coordinator that tunes the power ratio provided by each source according to the load and duty cycle estimations. Compared to our previous work [10], the MPC coordinator compute the optimal values of the power ratios by taking into account technical considerations and functional constraints. We can note that additional sensors and external information are not required to estimate the energy available. The performance of the system ensures a reserve to respond to the unexpected power variations. In our future works, both controllers (local stage and coordination stage) will be implemented into a Microchip dsPIC30F6010A microcontroller with industrial DC/DC converters. We will also improve the cost criteria to take into account economical and environmental performance. Our main efforts will finally be devoted on the supervised control with automata in order to find the optimal configurations according to the climatic conditions.

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